

Amendments to the Claims:

This listing of claims will replace all prior versions and listing of claims in the application.

Listing of Claims:

1. (Currently Amended) A wireless communication system comprising:
 - a filter; and
 - a semiconductor chip including a signal processing integrated circuit having an amplifier for receiving an output signal from said filter,
 - wherein a main surface of said semiconductor chip is provided with a plurality of electrode terminals along an edge portion thereof;
 - wherein said amplifier has a transistor including a control electrode to which the output signal of said filter is to be supplied, a first electrode through which a signal is outputted in accordance with the signal supplied to said control electrode, and a second electrode to which a voltage is applied;
 - wherein said control electrode, said first electrode and said second electrode of said transistor are connected to said electrode terminals through relatively short wirings therebetween, respectively; and
 - none of said wirings are arranged to traverse said wirings respectively connected between a first said electrode terminal and said first electrode of said transistor, between a second said electrode terminal and said second electrode of said transistor, and between a third said electrode terminal and said control electrode of said transistor.

2. (Original) A wireless communication system according to claim 1, wherein said semiconductor chip has said edge portion and an opposite side to said edge portion, and said electrode terminals, said transistor and a wiring through which a voltage is supplied to said transistor are in turn arranged from said edge portion towards said opposite side.

3. (Previously Amended) A wireless communication system according to claim 1, wherein the voltage applied to said second electrode terminal of said transistor is a ground voltage, and the second electrode terminal through which the ground voltage is supplied to said second electrode terminal of said transistor is different from one of the electrode terminals through which the ground voltage is supplied to electrostatic discharge protecting circuits provided for said transistor.

4. (Previously Amended) A wireless communication system according to claim 1, wherein said signal processing integrated circuit includes a signal transmitting circuit and the wiring through which a voltage is supplied to said signal transmitting circuit is coupled to the same one of the electrode terminals as that of the wiring through which a voltage is supplied to a bias circuit and said electrostatic discharge protecting circuits for said amplifier.

5. (Original) A wireless communication system according to claim 2, wherein said amplifier is present on said edge portion of said semiconductor chip and is arranged near the center of said edge portion.

6. (Original) A wireless communication system according to claim 2, wherein said amplifier is arranged at a corner part which is defined between said edge portion of said semiconductor chip and a crossing side which crosses said one edge portion.

7. (Previously Amended) A wireless communication system according to claim 2, wherein no wiring is arranged between said electrode terminals and said edge portion of said semiconductor chip.

8. (Original) A wireless communication system according to claim 1, wherein the wiring arranged from said electrode terminal is linked with said control electrode of said transistor and one electrode of protection diodes which are provided for said transistor.

9. (Original) A wireless communication system according to claim 3, wherein said amplifier is arranged between said edge portion of said semiconductor chip and said electrostatic discharge protecting circuit.

10. (Currently Amended) A wireless communication system comprising two signal processing circuit having different wavelengths, each of said signal processing circuits including:

a filter; and

a signal processing integrated circuit having an amplifier receiving an output signal from said filter,

wherein said two signal processing integrated circuits are formed on a semiconductor chip;

wherein a main surface of said semiconductor chip is provided with a plurality of electrode terminals along an edge portion thereof;

wherein each of said signal processing integrated circuits has a transistor including a control electrode to which the output signal of said filter to be supplied, a first electrode through which a signal is outputted in accordance with the signal supplied to said control electrode, and a second electrode to which a voltage is applied;

wherein said control electrode, said first electrode and said second electrode of each of said transistors are electrically connected to said electrode terminals through relatively short wirings therebetween, respectively; and

none of said wirings in each of said signal processing integrated circuits are arranged to traverse said wirings respectively connected between first ones of said electrode terminals and said first electrodes of said transistors, between

second ones of said electrode terminals and said second electrodes of said transistors, and between third ones of said electrode terminals and said control electrodes of said transistors.

11. (Original) A wireless communication system according to claim 10, wherein said semiconductor chip has said edge portion and an opposite side opposite to said edge portion, and said electrode terminals, said transistors and a wiring through which a voltage is supplied to said transistor are in turn arranged from said edge portion towards said opposite side.

12. (Previously Amended) A wireless communication system according to claim 10, wherein the voltage applied to said second electrode terminals of said transistor is a ground voltage, and the second electrode terminals through which the ground voltage is supplied to said second electrode terminals of said transistors is different from the electrode terminals through which the ground voltage is supplied to electrostatic discharge protecting circuits which are provided for said transistors.

13. (Original) A wireless communication system according to claim 10, wherein said each amplifier of said signal processing systems and said electrostatic discharge protecting circuits connected to said amplifier are provided in one area;

said one area has a contour which is surrounded with a side extending along said edge portion of said semiconductor chip, an opposite side opposite to said edge portion and sides linking said edge portion with said opposite side; and

said opposite side is of a power source line and a ground line of a contour which is changed step by step.

14. (Previously Amended) A wireless communication system according to claim 10, wherein said each amplifier is a low-noise amplifier.

15. (Original) A wireless communication system according to claim 10, wherein said amplifier of each of said signal processing systems and said electrostatic discharge protecting circuits connected to said amplifier are provided in one area;

said one area is provided at a predetermined interval between said edge portion of said semiconductor chip and an opposite side opposite to said edge portion; and

said opposite side is of a power source line and a ground line of a contour which is changed step by step.

16. (Previously Amended) A wireless communication system according to claim 10, wherein each said signal processing integrated circuit has a signal

transmitting circuit and a wiring through which a voltage is applied to said signal transmitting circuit is coupled to the same electrode terminal as that of the wiring through which a voltage is applied to a bias circuit and electrostatic discharge protecting circuits for said amplifier.

17. (Previously Amended) A wireless communication system according to claim 11, wherein each said amplifier is present on the side of said edge portion of said semiconductor chip and is arranged near the center of said edge portion.

18. (Original) A wireless communication system according to claim 11, wherein said amplifier is arranged in a corner part which is defined between said edge portion of said semiconductor chip and a crossing side which crosses said edge portion.

19. (Original) A wireless communication system according to claim 11, wherein no wiring is arranged between said electrode terminal and said edge portion of said semiconductor chip.

20. (Original) A wireless communication system according to claim 10, wherein the wiring arranged from said electrode terminal is linked with said control

electrode of said transistor and one electrode of electrostatic discharge protecting which are provided for said transistor.

21. (Original) A wireless communication system according to claim 20, wherein electrostatic discharge protecting circuit provided for said transistor are connected to said first electrode and said second electrode of said transistor, respectively.

22. (Previously Presented) A wireless communication system according to claim 1, wherein said semiconductor chip includes electrostatic discharge protecting circuits connected through a first wiring to said plurality of electrode terminals connected with said transistor, and wherein none of said wirings, except for said first wiring, are arranged to traverse said wirings connected between said first electrode terminal and said first electrode of said transistor, between said second electrode terminal and said second electrode of said transistor, and between said third electrode terminal and said control electrode of said transistor.

23. (Currently Amended) A wireless communication system according to claim 10, wherein said semiconductor chip includes electrostatic discharge protecting circuits connected through a first wiring to said plurality of electrode terminals connected with said transistor of said signal processing integrated circuit, and

wherein none of said wirings, except for said first wiring, are arranged to traverse said wirings connected between said first electrode terminals and said first electrodes of said transistors, between said second electrode terminals and said second electrodes of said transistors and between said third electrode terminals and said control electrodes of said transistors.

24. (Currently Amended) A wireless communication system according to claim 23, wherein one of said signal processing integrated circuits has no wirings, except for said first wiring, ~~are~~ arranged to traverse said wirings connected between said first electrode terminals and said first electrodes of said transistors, between said second electrode terminals and said second electrodes of said transistors and between said third electrode terminals and said control electrodes of said transistors.